

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Pendharkar (TI-34637)

Serial No. ____/____

Group Art Unit:

Filed: Herewith

Examiner:

For: Drain Extended MOS Transistor with Improved Breakdown Robustness

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicant brings the reference listed on the enclosed PTO/SB/08A to the attention of the Patent and Trademark Office relative to this application.

A copy of the reference is enclosed. The reference is in the English language. As such, no additional statement of relevance is provided in this paper.

Consideration of this information in the prosecution of this application is respectfully requested.

Respectfully submitted,



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TI-34637

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Aur and Chatterjee, "Robustness of LDD nMOS Transistors Subjected to Measurement of Drain Breakdown Voltage", <u>Extended Abstracts of the 22nd Conference on Solid State Devices and Materials</u> , Paper C-6-8 (1990), pp. 319-22.	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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